



Koneru Lakshmaiah Education Foundation

(Category -1, Deemed to be University estd. u/s. 3 of the UGC Act, 1956)

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Campus: Green Fields, Vaddeswaram - 522 302, Guntur District, Andhra Pradesh, INDIA.
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A Report on Zrotriya -23 Department of ECE Technical Fest



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Admin Off: 29-36-38, Museum Road, Governorpet, Vijayawada - 520 002. Ph: +91 - 866 - 3500122, 2576129

Date of the Event : 29-02-2023
Name of the Event : AWR
Venue : R106(Lab)
No. of Participants : Students: 25, Staff: 04

A Report on AWR on 29-02-2023

Objective:

An AWR technical event aims to empower you with the knowledge and skills to manage your home or office wifi network like a pro. Through the workshop, you'll delve into configuring your AWR to perfectly suit your needs. This includes setting up security measures, creating guest networks, implementing parental controls, and prioritizing network traffic (QoS) for optimal performance. Troubleshooting common AWR issues like signal weakness, dropped connections, and interference will also likely be covered.

Description: KL Deemed to be University (Koneru Lakshmaiah Education Foundation) Department of ECE conducted “AWR” program on 29-02-2023 at R&D 106(LAB). We started the event at 9:30AM.

With the intention of educating the students regarding basic electronics, Students are very eagerly learning and were very interested in taking part in their given problem statements.

Under the guidance of MR.B.Sri.Kanth Deepak, Mr.P Sundariah, this event was very successful. Mr. P. Srikanth Reddy (Program Coordinator, KLEF PULSE) also took part in the event and together made the event a grand success.

Introduction: Pulse, the student body of the Electronics and Communication Engineering (ECE) department, hosted an engaging technical event as part of the Zrotriya National Level Technical Fest. Titled "AWR Technical Workshop: Mastering Your Home or Office WiFi Network," the event aimed to equip participants with the knowledge and skills needed to manage their AWR (Advanced Wireless Router) effectively.

Event Overview: The workshop delved into the intricacies of configuring AWRs to meet specific requirements, whether for home or office networks. Participants were guided through various aspects, including setting up security measures, creating guest networks, implementing parental controls, and prioritizing network traffic (Quality of Service - QoS) for optimal performance. Moreover, the workshop addressed common AWR issues such as signal weakness, dropped connections, and interference, offering troubleshooting techniques to resolve them.

Key Highlights:

Comprehensive Guidance: Participants received comprehensive guidance on configuring AWRs to align with their unique needs and preferences. From enhancing security to optimizing network performance, the workshop covered various aspects of AWR management in detail. Hands-on

Demonstrations: Hands-on demonstrations were conducted to provide participants with practical insights into AWR configuration processes. Attendees had the opportunity to interact with AWR interfaces and learn firsthand how to implement security measures, create guest networks, and troubleshoot common issues. Interactive

Q&A Sessions: Interactive Q&A sessions encouraged participants to seek clarification on specific AWR-related queries and challenges they faced in managing their networks. Experienced instructors provided insightful responses and practical solutions to address participants' concerns effectively.

Real-world Scenarios: The workshop incorporated real-world scenarios to illustrate the relevance and applicability of AWR management techniques. Participants were presented with case studies reflecting common networking challenges encountered in home and office environments, enabling them to apply their newfound knowledge in practical settings.

Networking Opportunities: The event provided networking opportunities for participants to connect with peers, exchange ideas, and share experiences related to AWR management. Engaging discussions fostered a collaborative learning environment and encouraged knowledge sharing among participants.

Conclusion: The "AWR Technical Workshop: Mastering Your Home or Office WiFi Network" organized by Pulse during the Zrotriya National Level Technical Fest proved to be an enriching and insightful experience for participants. By equipping attendees with the knowledge and skills to manage AWRs effectively, the workshop empowered them to enhance the security, performance, and reliability of their home or office networks. **Acknowledgments:** The success of this event owes much to the dedication and efforts of the organizing committee, volunteers, instructors, and participants. Their enthusiasm and commitment played a pivotal role in creating a valuable learning experience for all involved. **Future Prospects:** Pulse remains committed to organizing similar technical workshops and events in the future, catering to the evolving needs and interests of ECE students and enthusiasts. By continuing to explore emerging technologies and trends in the field of electronics and communication.

Outcome:

An AWR technical event empowers you to transform your home or office Wi-Fi from frustrating to fantastic. Through the workshop, you'll gain the knowledge to configure your AWR for optimal performance. This includes setting up robust security to safeguard your network, creating guest networks, and implementing parental controls. You'll also learn to prioritize traffic (QoS) for a lag-free experience. But that's not all! The workshop dives into advanced features like beamforming for focused signals, MU-MIMO for handling multiple devices, and mesh networking for expansive coverage. Finally, you'll develop troubleshooting skills to conquer common issues like weak signals and dropped connections. By the end, you'll be a Wi-Fi whiz, ensuring a smooth and reliable network for everyone. .

Event Photos:



Faculty giving suggestions to the event co-ordinators

In the below pictures we made the students as a groups as their wish and give a troubleshoot to solve it, meanwhile faculty came to take part in it. The pictures are representing the review sessions going on in the event where the students are awarded with marks

S.NO	NAME	ID NUMBER	BRANCH
1	HARSHITHA. D	2200040212	ECE <i>D.H.</i>
2	MANJU VANIK	2200040057	ECE <i>Manu.K</i>
3	SAI LAKSHMI	2200049054	ECE <i>Lakshmi.K</i>
4	KAMALESH	2200049078	ECE <i>KAMLESH</i>
5	T.M. MANOJ	2200040360	ECE <i>MANOJ</i>
6	SONY SRI SUBHA	2200040077	ECE <i>Sri Subha</i>
7	ANKA RAO	2200040096	ECE <i>ANKA</i>
8	HARI PRASAD	2200040028	ECE <i>Hari</i>
9	vevak	2200040214	ECE <i>Venik</i>
10	G CHETAN	2100040160	ECE <i>Chet</i>
11	U AJAY	2100049001	ECE <i>Ajay</i>
12	A VINITHA	2100040176	ECE <i>Vinitha</i>
13	SK ALIM BASHEER	2100040290	ECE <i>SK</i>
14	R KARTHIK	2100040134	ECE <i>R.K.K</i>
15	P AASRITHA	2100040240	ECE <i>Aashritha</i>
16	B YESHASWINI	2100040347	ECE <i>Yeshaswini</i>
17	P Karthik	2100040125	ECE <i>P. Karthik</i>
18	K Chetan	2100040124	ECE <i>K Chetan</i>
19	M Ankita	2100040314	ECE <i>Ankitha</i>
20	CH AARYANI	2100040194	ECE <i>CH. AARYANI</i>
21	SK JHANI	2100049089	ECE <i>SK. Jhoni</i>
22	M MANOHAR	2100040131	ECE <i>M. Manohar</i>
23	G DHEERAJ	2100040233	ECE <i>Dheeraj</i>
24	N VIVEK KUMAR	2100040273	ECE <i>Kumar</i>
25	CH V DEEPAK	2100040149	ECE <i>Deepak.</i>



Students participating in the AWR Event



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Admin Off: 29-36-38, Museum Road, Governorpet, Vijayawada - 520 002. Ph: +91 - 866 - 3500122, 2576129

Date of the Event : 28-02-2023
Name of the Event : BUGSPOT
Venue : R107(Lab)
No. of Participants : Students: 52, Staff: 04

A Report on BUGSPOT

on 28-02-2023

Objective:

The objective of the BUGSPOT event centered around fostering innovation and problem-solving within the realm of system-on-chip (SoC) technology. Participants were tasked with identifying, diagnosing, and resolving bugs or issues within complex SoC designs. Through this challenge, the event aimed to cultivate participants' analytical skills, technical proficiency, and teamwork abilities while promoting creativity and ingenuity in addressing real-world SoC challenges.

Description: KL Deemed to be University (Koneru Lakshmaiah Education Foundation) Department of ECE conducted “BUGSPOT” program on 28-02-2023 at R&D 107(LAB). We started the event at 9:30AM.

With the intention of educating the students regarding basic electronics, Students are very eagerly learning and were very interested in taking part in their given problem statements.

Under the guidance of Mr. I. Veera Raghava Rao, Ms. C.Priyanka, this event was very successful. Mr. P. Srikanth Reddy (Program Coordinator, KLEF PULSE) also took part in the event and together made the event a grand success.

Organizer: Pulse - ECE Department Student Body

Introduction:

Pulse, the student body of the Electronics and Communication Engineering (ECE) department, orchestrated an innovative event titled "BUGSPOT" during the Zrotriy National Level Technical Fest. This event was meticulously crafted with the objective of fostering innovation and problem-solving within the realm of System-on-Chip (SoC) technology.

Event Overview:

BUGSPOT aimed to challenge participants' analytical skills, technical proficiency, and teamwork abilities through the identification, diagnosis, and resolution of bugs or issues within complex SoC designs. By delving into real-world SoC challenges, the event sought to cultivate creativity and ingenuity among participants while providing a platform for collaborative problem-solving.

Key Highlights:

Challenge Format:

Participants were presented with intricate SoC designs containing hidden bugs or issues. Their task was to meticulously analyze the designs, identify the anomalies, and devise effective solutions to rectify the detected bugs. The challenge format encouraged participants to employ a combination of theoretical

knowledge, practical expertise, and innovative thinking to overcome obstacles.

Team Collaboration:

The event emphasized the importance of teamwork, as participants worked collaboratively in teams to unravel the complexities of SoC designs. By fostering interdisciplinary collaboration and communication, BUGSPOT encouraged participants to leverage each other's strengths and perspectives in tackling challenging scenarios.

Technical Proficiency:

BUGSPOT provided participants with a platform to showcase their technical prowess in SoC design and debugging. Participants utilized state-of-the-art tools and methodologies to analyze and debug complex hardware and software interactions within SoC architectures. The event served as a testament to participants' proficiency in navigating the intricacies of modern electronic systems.

Innovation and Creativity:

Participants were encouraged to think outside the box and explore innovative solutions to address SoC bugs effectively. Through creative problem-solving approaches, participants demonstrated their ability to adapt to dynamic challenges and devise novel strategies for bug resolution. BUGSPOT served as a catalyst for cultivating a culture of innovation and ingenuity within the ECE community.

Conclusion:

BUGSPOT emerged as a captivating and intellectually stimulating event, showcasing the prowess of participants in tackling real-world SoC challenges. The event not only honed participants' analytical skills and technical proficiency but also fostered a spirit of teamwork, innovation, and creativity within the ECE community. As technology continues to advance, events like BUGSPOT play a crucial role in preparing future engineers to tackle the complexities of SoC design and contribute meaningfully to the field of electronics engineering.

Acknowledgments:

The success of BUGSPOT would not have been possible without the dedicated efforts of Pulse, the ECE department student body, as well as the enthusiastic participation of all attendees. Their collective commitment to excellence and passion for innovation contributed to making BUGSPOT a memorable and enriching experience for all involved.

Future Prospects:

Moving forward, Pulse is committed to organizing more such events that challenge participants to push the boundaries of innovation and problem-solving in electronic engineering. BUGSPOT has set a precedent for fostering collaboration, creativity, and technical excellence within the ECE community, and future editions of the event are poised to build upon this foundation and inspire the next generation of electronic engineers.

Outcome:

The outcomes of the BUGSPOT event culminated in the successful identification and resolution of bugs within various system-on-chip (SoC) designs, showcasing participants' adeptness in problem-solving and technical proficiency. Participants gained valuable hands-on experience in diagnosing and addressing complex SoC issues, furthering their skills and knowledge in this specialized field. Moreover, the event fostered collaboration and teamwork among participants, promoting the exchange of ideas and best practices. The successful resolution of bugs not only contributed to the advancement of SoC technology but also provided participants with a sense of accomplishment and validation of their capabilities within the field.

Event Photos:



Faculty involved in this event and they came front to interview and conducted a HR round for the students.



S.NO	NAME	ID NUMBER	BRANCH
1	A VINITHA	2100040176	ECE <i>Vinitha</i>
2	SK ALIM BASHEER	2100040290	ECE <i>Basheer</i>
3	R KARTHIK	2100040134	ECE <i>Karthik</i>
4	P AASRITHA	2100040240	ECE <i>Aasritha</i>
5	B YESHASWINI	2100040347	ECE <i>Yes</i>
6	P Karthik	2100040125	ECE <i>Karthik</i>
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13	N VIVEK KUMAR	2100040273	ECE <i>Vivek</i>
14	CH V DEEPAK	2100040149	ECE <i>Deepak</i>
15	V V K S KARTHIKEYA	2100049087	ECE <i>Karthikeya</i>
16	V LALITHA SREE	2100040163	ECE <i>Sree</i>
17	K DEEPTHI SREE	2100049009	ECE <i>Deepti</i>
18	SHASHANK. K	2200040330	ECE <i>Shashank</i>
19	V.ROHIT	2200040231	ECE <i>Rohit</i>
20	B.MOUNIKA	2200040315	ECE <i>Mounika</i>
21	M Gokul	2200040235	ECE <i>Gokul</i>
22	SUBRAHMANYA SAI	2200040234	ECE <i>Sai</i>
23	M.K.RAHUL	2200040121	ECE <i>Rahul</i>
24	Y.RAKESH	2200040260	ECE <i>Rakesh</i>
25	JAHANVI. K	2200040065	ECE <i>Jahanvi</i>
26	JYOTHSNA. K	2200040195	ECE <i>Jyotsna</i>
27	VASHMI	2200040157	ECE <i>Vashmi</i>
28	HARIHARAN	2200040063	ECE <i>Hariharan</i>
29	R.SIVESH	2200040033	ECE <i>Sivesh</i>
30	T.RAJESH	2200040257	ECE <i>Rajesh</i>
31	NAGA SAI RAM. G	2200040109	ECE <i>Sai Ram</i>
32	HARSHITHA. D	2200040212	ECE <i>Harshitha</i>
33	MANJU VANIK	2200040057	ECE <i>Manju</i>
34	SAI LAKSHMI	2200049054	ECE <i>Lakshmi</i>
35	KAMALESH	2200049078	ECE <i>Kamalesh</i>
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40	vevak	2200040214	ECE <i>Vevak</i>



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A Report on CIRCUITRIX on 01-03-2023

Objective:

The objective of CIRCUITRIX was to provide participants with a platform to showcase their proficiency in electrical circuit design, analysis, and troubleshooting, fostering learning, skill development, collaboration, and inspiration within the field of electrical engineering.

Description: KL Deemed to be University (Koneru Lakshmaiah Education Foundation) Department of ECE conducted “CIRCUITRIX” program on 01-03-2023 at R&D 103(LAB). We started the event at 9:20AM.

With the intention of educating the students regarding basic electronics, Students are very eagerly learning and were very interested in taking part in their given problem statements.

Under the guidance of J. Lakshmi Prasanna, Mrs. K.V.Sowmya, this event was very successful. Mrs. Sowmya (Program Coordinator, KLEF PULSE) also took part in the event and together made the event a grand success.

The outcomes of CIRCUITRIX encompassed the recognition of winners for their prowess in electrical circuit design, analysis, and troubleshooting, alongside the enhancement of participants' skills and knowledge in the field. Networking opportunities facilitated valuable connections among enthusiasts, students, educators, and professionals, fostering collaboration and knowledge exchange. Feedback collected from stakeholders aimed at refining future editions, ensuring continual improvement. Moreover, the event served as a source of inspiration and motivation, encouraging participants to pursue further exploration and opportunities within the realm of electrical engineering.

Event Photos:



Faculty Conducting Reviews to students



In this picture we made the students as a groups as their wish and give a troubleshoot to solve it, meanwhile faculty came to take part in it.

Event Structure:

CIRCUITRIX comprised several competitive rounds designed to test participants' capabilities across different areas of electrical engineering. The event featured challenges ranging from basic circuit design to complex troubleshooting scenarios, ensuring a comprehensive assessment of participants' skills.

Preliminary Round: Participants were required to solve theoretical problems related to electrical circuits, demonstrating their understanding of fundamental concepts.

Design Challenge: In this round, participants were tasked with designing circuits to meet specific requirements, such as voltage regulation, signal amplification, or frequency filtering. Creativity, efficiency, and adherence to design constraints were key evaluation criteria.

Simulation Round: Participants used simulation software to analyze and optimize circuit performance under different conditions. This round tested participants' ability to interpret simulation results and make informed design decisions.

Troubleshooting Round: This round presented participants with malfunctioning circuits, and they were required to identify and rectify faults within a stipulated time frame. Effective problem diagnosis, logical reasoning, and swift decision-making were crucial in this round.

Presentation Round: Finalists presented their circuit designs, analysis techniques, and problem-solving approaches to a panel of judges. Participants were evaluated based on the clarity of their presentation, depth of understanding, and ability to articulate their ideas effectively. Moreover, the event served as a source of inspiration and motivation, encouraging participants to pursue further exploration and opportunities within the realm of electrical engineering.

Outcome:

CIRCUITRIX achieved its objective of providing a platform for participants to showcase their skills and knowledge in electrical engineering. The event not only assessed participants' technical proficiency but also encouraged collaboration, as participants exchanged ideas and strategies throughout the competition.

Moreover, CIRCUITRIX fostered a spirit of innovation and creativity by challenging participants to devise efficient solutions to real-world problems. Participants gained valuable insights into the practical application of electrical engineering principles and developed critical thinking skills essential for the field.

Conclusion:

CIRCUITRIX, as part of “**Zrotriya**” played a pivotal role in promoting excellence in electrical engineering by nurturing talent, encouraging skill development, and fostering a culture of innovation. The event provided participants with a platform to demonstrate their abilities, collaborate with peers, and gain invaluable experience in tackling real-world engineering challenges. Moving forward, initiatives like CIRCUITRIX are essential for nurturing the next generation of electrical engineers and driving technological advancement in the field.



2022
ZROTRIYA
A NATIONAL LEVEL TECHNICAL FEST

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CIRCUITRIX

DATE
MARCH 1st

VENUE
R210D



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3	R KARTHIK	3100030131	ECE Karthik
4	P AASRITHA	3100030165	ECE Aasritha
5	B YESHASWINI	3100039008	ECE Yesh
6	P Karthik	3100030127	ECE Pk
7	K Chetan	3100030180	ECE Chetan
8	M Ankit	3100030207	ECE Ankit
9	CH AARYANI	3100030157	ECE Aaryani
10	SK JHANI	3100030160	ECE Sk Jhani
11	M MANOHAR	3100039001	ECE Manu
12	G DHEERAJ	3100030176	ECE Gaj
13	N VIVEK KUMAR	3100030390	ECE Nvivek
14	CH V DEEPAK	3100030123	ECE Deepak
15	V V K S KARTHIKEYA	3100030330	ECE VVKS
16	V LALITHA SREE	3100030237	ECE Sree
17	K DEEPTHI SREE	3100030135	ECE Deepa
18	SHASHANK. K	3100030133	ECE Shashank
19	V.ROHIT	3100030213	ECE Rohit
20	B.MOUNIKA	3100030203	ECE Mouni
21	M Gokul	3100039089	ECE Gokul
22	SUBRAHMANYA SAI	3100030121	ECE Sai
23	M.K.RAHUL	3100030322	ECE Rahul
24	Y.RAKESH	3100030372	ECE Rakesh
25	JAHANVI. K	3100030139	ECE Jahanvi
26	JYOTHSNA. K	3100039087	ECE Jyotsna
27	VASHMI	3100030162	ECE Vashmi
28	HARIHARAN	3100039009	ECE Hari
29	R.SIVESH	2000030220	ECE Sivesh
30	T.RAJESH	2000030321	ECE Rajesh
31	NAGA SAI RAM. G	2000030215	ECE Sai Ram
32	HARSHITHA. D	2000030325	ECE Harshitha
33	MANJU VANIK	2000030323	ECE Manju
34	SAI LAKSHMI	2000030131	ECE Sai
35	KAMALESH	2000030360	ECE Kamal
36	T.M. MANOJ	2000030065	ECE Manu
37	SONY SRI SUBHA	2000030205	ECE Sony
38	ANKA RAO	2000030157	ECE Anka
39	HARI PRASAD	2000030062	ECE Hari
40	vevak	2000030022	ECE Vevak

41	D.SANDEEP REDDY	2000030357	ECE	Sandey
42	D UMA NAGA SAI	2000030109	ECE	cuma
43	K.SRI VALLIKA	2000030313	ECE	Sri Vallika
44	G SASI NAGA KIRAN	2000030057	ECE	G Sasi?
45	P SUMANTHI	2000039053	ECE	P Sumanti
46	K SAI TEJA	2000039078	ECE	Sai
47	M LAVANYA	2000030260	ECE	M Lavanya
48	D SRAVANI	2000030077	ECE	Sri Ran
49	S SRI RAM	2000030096	ECE	Sri Ram
50	G CHIETAN	2000030038	ECE	Chetan
51	U AJAY	2000030313	ECE	Ajay



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Date of the Event : 27-02-2023
Name of the Event : Knights of NI
Venue : R304A
No. of Participants : Students: 40, Staff: 03

A Report on Knights of NI

on 27-02-2023

Objective:

The objective of the passage is to express gratitude and admiration towards the Knights of NI, individuals who contribute knowledge and insight to online discourse. It aims to highlight their role as guardians of truth and champions of enlightenment in the digital realm, emphasizing their impact in fostering intellectual engagement, curiosity, and the pursuit of knowledge among online communities.

Description:

In the esteemed Department of ECE at KL Deemed to be University, the "Knights of NI" program unfolded on march 19, 2024, at R&D 304A, commencing at 9:30 AM.

Guided by Dr. G. Naveen Kishore and Mr. D. Pardha Saradhi, the event aimed to educate students on basic electronics. Under their expert guidance, students enthusiastically engaged with the provided problem statements, displaying eagerness to learn and participate.

The presence of Mr. P. Srikanth Reddy, Program Coordinator of Pulse KLEF, further enriched the event, contributing to its success.

"Knights of NI" served as a testament to intellectual engagement and the pursuit of

knowledge in the digital age. Participants expressed gratitude towards the organizers for their dedication to fostering enlightenment in online discourse.

Guidance and Support:

The expert guidance provided by Dr. Kishore and Mr. Saradhi played a crucial role in nurturing enthusiasm and facilitating a conducive learning environment. The presence of Mr. P. Srikanth Reddy, Program Coordinator of Pulse KLEF, further enriched the experience, highlighting the importance of community engagement in educational initiatives.

Gratitude and Recognition:

Participants expressed sincere gratitude towards the organizers for their dedication to enlightenment in online discourse, emphasizing the vital role played by individuals in fostering intellectual curiosity and the pursuit of knowledge in the digital age.

Outcome:

The "Knights of NI" event at KL Deemed to be University served as a catalyst for heightened recognition and appreciation of the invaluable contributions made by individuals to online discourse. Through interactive sessions and problem-solving activities, attendees were inspired to engage intellectually, fostering vibrant dialogues and strengthening bonds within online communities. The event emphasized the promotion of positive and respectful discussion norms, encouraging attendees to uphold these standards in their online interactions. By showcasing exemplary behavior and acknowledging the efforts of the Knights of NI, the event served as a source of encouragement for continued contributions, collectively fostering an enriched and constructive digital discourse environment. Moreover, attendees were motivated to actively participate in online discourse, recognizing the significance of their contributions in shaping digital conversations and disseminating knowledge. The event facilitated the formation of supportive networks among participants, fostering collaboration and mentorship opportunities that extend beyond the confines of the program.

Event Photos:



Here we can observe by these two picture that our senior members were involved in this event and they came front to interview and conducted a HR round for the students.



In this picture we made the students as a group as their wish and give a troubleshoot to solve it, meanwhile faculty came to take part in it.

Conclusion:

The "Knights of NI" program exemplified a commitment to nurturing intellectual curiosity and fostering a culture of learning. Through collaborative efforts and expert guidance, the event successfully promoted enlightenment and knowledge dissemination in the digital realm.

S.NO	NAME	ID NUMBER	BRANCH
1	SK JHANI	2100049089	ECE
2	M MANOHAR	2100040131	ECE
3	G DHEERAJ	2100040233	ECE
4	N VIVEK KUMAR	2100040273	ECE
5	CH V DEEPAK	2100040149	ECE
6	V V K S KARTHIKEYA	2100049087	ECE
7	V LALITHA SREE	2100040163	ECE
8	K DEEPTHI SREE	2100049009	ECE
9	SHASHANK. K	2200040330	ECE
10	V.ROHIT	2200040231	ECE
11	D.SANDEEP REDDY	2100040151	ECE
12	D UMA NAGA SAI	2100040158	ECE
13	K.SRI VALLIKA	2100040141	ECE
14	G SASI NAGA KIRAN	2100040165	ECE
15	P SUMANTH	2100049008	ECE
16	K SAI TEJA	2100040137	ECE
17	M LAVANYA	2100040180	ECE
18	D SRAVANI	2100040227	ECE
19	S SRI RAM	2100040157	ECE
20	G CHETAN	2100040160	ECE
21	U AJAY	2100049001	ECE
22	A VINITHA	2100040176	ECE
23	SK ALIM BASHEER	2100040290	ECE
24	R KARTHIK	2100040134	ECE
25	P AASRITHA	2100040240	ECE
26	B YESHASWINI	2100040347	ECE
27	P Karthik	2100040125	ECE
28	K Chetan	2100040124	ECE
29	M Ankita	2100040314	ECE
30	CH AARYANI	2100040194	ECE
31	B.MOUNIKA	2200040315	ECE
32	M Gokul	2200040235	ECE
33	SUBRAHMANYA SAI	2200040234	ECE
34	M.K.RAHUL	2200040121	ECE
35	Y.RAKESH	2200040260	ECE
36	JAHANVI. K	2200040065	ECE
37	JYOTHSNA. K	2200040195	ECE
38	VASHMI	2200040157	ECE
39	HARIHARAN	2200040063	ECE
40	R.SIVESH	2200040033	ECE

Jhoni
 Manu
 Dhruv
 Vivek
 Deepak
 Karthi
 Lalitha
 Deepthi
 Shashank
 Rohit
 Sandeep
 Uma Naga
 Vallika
 Kiran
 Sumanth
 Sai Teja
 Lavanya
 Sravani
 Sri Ram
 Chetan
 Ajay
 Vinitha
 Alim
 Karthik
 Aasritha
 Yeshaswini
 Karthik
 Chetan
 Ankita
 Aaryani
 Mounika
 Gokul
 Subhmanya
 Rahul
 Rakesh
 Jahanvi
 Jyothsna
 Vashmi
 Hariharan
 Rivesh



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Admin Off: 29-36-38, Museum Road, Governorpet, Vijayawada - 520 002. Ph: +91 - 866 - 3500122, 2576129

Date of the Event	: 26-02-2023
Name of the Event	: NI MyDaq WORKSHOP
Venue	: R-6 TH FLOOR (Lab)(COE)
No. of Participants	: Students: 32, Staff: 04

A Report on NI MYDAO on

26-02-2023

Objective:

An NI MyDaq workshop aims to equip participants with the skills to utilize National Instruments' MyDaq data acquisition system. The workshop likely provides a hands-on approach, familiarizing attendees with the MyDaq hardware and software, enabling them to configure the system for different data acquisition tasks and effectively interpret the collected measurements.

Description: KL Deemed to be University (Koneru Lakshmaiah Education Foundation) Department of ECE conducted “NI MyDaq WORKSHOP” program on 26-02-2023 at R&D 6th floor (COE)(LAB). We started the event at 9:30AM.

With the intention of educating the students regarding basic electronics, Students are very eagerly learning and were very interested in taking part in their given problem statements.

Under the guidance of Dr.P.Pardha saradhi, Mr.Namgiri Suresh, this event was very successful. Mr. P. Srikanth Reddy (Program Coordinator, KLEF PULSE) also took part in the event and together made the event a grand success.

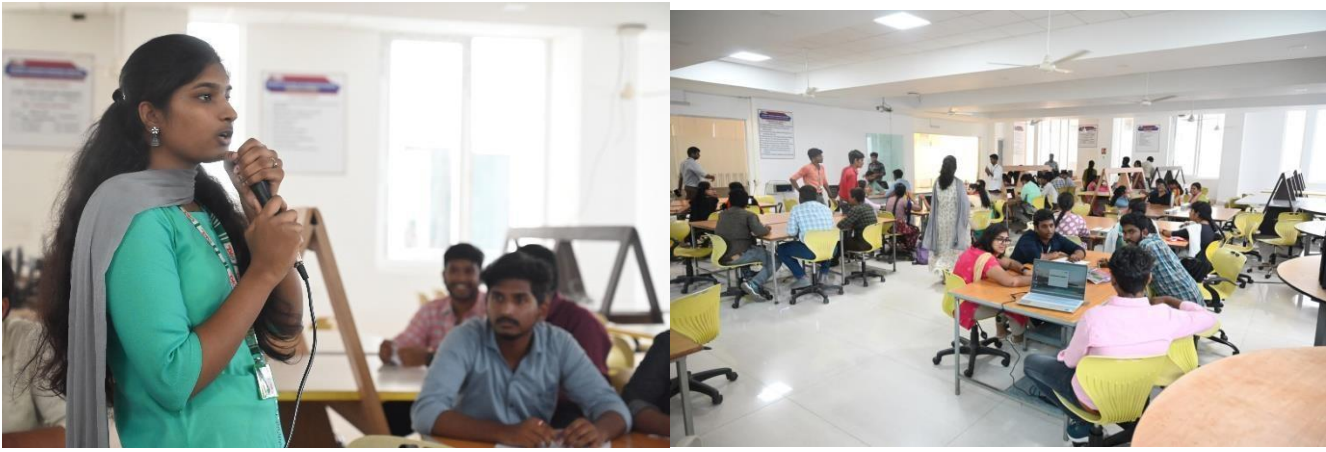
Outcome:

An NI MyDaq workshop offers a valuable learning experience for those interested in data acquisition. Through the workshop, you'll gain hands-on skills in using National Instruments' MyDaq system. This includes learning how to configure the hardware and software to effectively collect data from various sensors and instruments. You'll also delve into methods for analyzing the acquired measurements, allowing you to extract meaningful insights from the data. By the workshop's conclusion, you'll be well-equipped to utilize the MyDaq system for your own data acquisition projects.

Event Photos:



Here we can observe by these two picture that our Honorable faculty members were involved in this event and they came front to interview and conducted a HR round for the students.



In this picture we made the students as a groups as their wish and give a troubleshoot to solve it, meanwhile faculty came to take part in it.

- Event Coordinator : Dr.P.Pardha saradhi
- Technical Co Ordinator : Namgiri Suresh

The Zrotriya National Level Technical Fest celebrated the realm of electronics with its flagship event titled "Advancements in Electronics: A Glimpse into the Future." This event was meticulously organized by the Electronics Branch of the fest, aiming to provide participants with insights into cutting-edge developments in electronic technologies.

Event Overview: The event comprised various segments tailored to cater to the diverse interests and knowledge levels of participants. From keynote presentations by industry experts to hands-on workshops and competitive challenges, the event encapsulated the essence of contemporary electronic innovations.

1. Keynote Presentations: Renowned experts from academia and industry delivered keynote addresses, shedding light on emerging trends and future prospects in electronics. Topics ranged from quantum computing and nanotechnology to Internet of Things (IoT) and artificial intelligence (AI) applications in electronics.

2. **Workshops:** Practical workshops were conducted to impart valuable skills and knowledge to participants. Topics included PCB design, embedded systems programming, robotic automation, and 3D printing in electronics manufacturing. These workshops provided attendees with the opportunity to delve into hands-on learning experiences under the guidance of experienced instructors.
 3. **Technical Competitions:** The event hosted a series of technical competitions designed to challenge participants' ingenuity and problem-solving abilities. Contests such as circuit design challenges, robotics competitions, and hackathons tested participants' creativity and technical prowess. Winners were rewarded with accolades and prizes, motivating participants to showcase their best efforts.
 4. **Exhibition:** An exhibition showcasing the latest electronic gadgets, prototypes, and research projects was a major attraction of the event. Participants had the opportunity to interact with exhibitors, explore innovative technologies, and gain insights into real-world applications of electronics.
 5. **Panel Discussions:** Engaging panel discussions were organized on pertinent topics such as sustainable electronics, ethical considerations in technology development, and the future of consumer electronics. These discussions fostered intellectual discourse and encouraged participants to critically analyze the societal impact of electronic advancements.
- Conclusion:** "Advancements in Electronics: A Glimpse into the Future" emerged as a resounding success, captivating the imagination of participants and fostering a spirit of innovation and collaboration. The event not only showcased the latest developments in electronics but also provided a platform for networking, learning, and inspiration.

As technology continues to evolve at a rapid pace, events like these play a pivotal role in shaping the future of electronic engineering and fostering a vibrant ecosystem of technological innovation. **Acknowledgments:** The success of this event would not have been possible without the unwavering support of the organizing committee, volunteers, sponsors, speakers, and participants.

Their collective efforts and enthusiasm contributed to making this event a memorable and enriching experience for all involved. Future Prospects: As the field of electronics continues to evolve, the Zrotriya National Level Technical Fest is committed to organizing more such events to keep pace with the dynamic nature of technology and provide a platform for fostering creativity, collaboration, and excellence in the field of electronics engineering.

S.NO	NAME	ID NUMBER	BRANCH
1	HARSHITHA. D	2200040212	ECE
2	MANJU VANI.K	2200040057	ECE
3	SAI LAKSHMI	2200049054	ECE
4	KAMALESH	2200049078	ECE
5	T.M. MANOJ	2200040360	ECE
6	SONY SRI SUBHA	2200040077	ECE
7	ANKA RAO	2200040096	ECE
8	HARI PRASAD	2200040028	ECE
9	vevak	2200040214	ECE
10	G CHETAN	2100040160	ECE
11	U AJAY	2100049001	ECE
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17	P Karthik	2100040125	ECE
18	K Chetan	2100040124	ECE
19	M Ankita	2100040314	ECE
20	CH AARYANI	2100040194	ECE
21	SK JHANI	2100049089	ECE
22	M MANOHAR	2100040131	ECE
23	G DHEERAJ	2100040233	ECE
24	N VIVEK KUMAR	2100040273	ECE
25	CH V DEEPAK	2100040149	ECE
26	V V K S KARTHIKEYA	2100049087	ECE
27	V LALITHA SREE	2100040163	ECE
28	K DEEPTHI SREE	2100049009	ECE
29	SHASHANK. K	2200040330	ECE
30	V.ROHIT	2200040231	ECE
31	B.MOUNIKA	2200040315	ECE
32	M Gokul	2200040235	ECE

Manju
 Sai
 Kam
 Manoj
 Sony
 Anka
 Hari
 Vevak
 Chetan
 Ajay
 SK ALIM
 Karthik
 Verhaswini
 Karthik
 Chetan
 Ankitha
 Aaryani
 Jhani
 Manohar
 Dheeraj
 Vivek
 VVK
 Lalitha
 Sreelakshmi
 Shashank
 Rohit
 Gokul



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Admin Off: 29-36-38, Museum Road, Governorpet, Vijayawada - 520 002. Ph: +91 - 866 - 3500122, 2576129

Date of the Event : 25-02-2023
Name of the Event : PAPER PRO
Venue : R104(Lab)
No. of Participants : Students: 51, Staff: 04

A Report on POSTERPRESENTATION

on 25-02-2023

Introduction:

Zrotriya, a prestigious national-level techno fest, organized by Team Zrotriya, hosted an exhilarating event centered around poster designing. The event aimed to provide a platform for participants to showcase their creativity, innovation, and design skills through visually captivating posters. The PAPER PRO event not only celebrated artistic expression but also encouraged participants to communicate complex ideas effectively through graphic design.

Objective:

The primary objective of the PAPER PRO event was to foster creativity and innovation among participants while emphasizing the importance of visual communication in conveying technical concepts. By challenging participants to design posters on diverse themes related to science, technology, engineering, and mathematics (STEM), the event aimed to promote interdisciplinary collaboration and inspire novel approaches to problem-solving.

Description:

KL Deemed to be University (Koneru Lakshmaiah Education Foundation) Department of ECE conducted “PAPER PRO” program on 25-02-2023 at R&D 104(LAB). We started the event at 9:30AM.

With the intention of educating the students regarding basic electronics, Students are very eagerly learning and were very interested in taking part in their given problem statements.

Under the guidance of B Sai Sandeep,G L P Ashok, this event was very successful. Mr. P. Srikanth Reddy (Program Coordinator, KLEF PULSE) also took part in the event and together made the event a grand success.

The outcomes of “**PAPER PRO**” encompassed the recognition of winners for their prowess in electrical circuit design, analysis, and troubleshooting, alongside the enhancement of participants' skills and knowledge in the field. Networking opportunities facilitated valuable connections among enthusiasts, students, educators, and professionals, fostering collaboration and knowledge exchange. Feedback collected from stakeholders aimed at refining future editions, ensuring continual improvement. Moreover, the event served as a source of inspiration and motivation, encouraging participants to pursue further exploration and opportunities within the realm of electrical engineering.

Event Photos:



Here we can observe by these two picture that our Honorable faculty members were involved in this event and they came front to interview and conducted a HR round for the students.



In this picture we made the students as a groups as their wish and give a troubleshoot to solve it, meanwhile faculty came to take part in it.

Moment was captured in a single frame, encapsulating the essence of the PAPER PRO event. In the photograph, a group of enthusiastic participants is seen engaged in an animated discussion, surrounded by stacks of papers and research materials. The intense expressions on their faces reflect the intellectual fervor and passion for knowledge exchange that permeated the event. With hands gesturing emphatically and minds abuzz with ideas, the participants epitomize the spirit of academic inquiry and collaboration fostered by PAPER PRO. This snapshot serves as a poignant reminder of the profound impact of scholarly endeavors and the transformative power of sharing knowledge within the dynamic milieu of Zrotriya.

JUDGES :

- 1)Dr K. Srinivasa Rao
- 2) Dr.Phani Kishore
- 3)Dr.Rehman
- 4)Dr.K.S.Ramesh

Event Structure:

PAPER PRO comprised several competitive rounds designed to test participants' capabilities across different areas of electrical engineering. The event featured challenges ranging from basic circuit design to complex troubleshooting scenarios, ensuring a comprehensive assessment of participants' skills.

Preliminary Round: Participants were required to solve theoretical problems related to electrical circuits, demonstrating their understanding of fundamental concepts.

Design Challenge: In this round, participants were tasked with designing circuits to meet specific requirements, such as voltage regulation, signal amplification, or frequency filtering. Creativity, efficiency, and adherence to design constraints were key evaluation criteria.

Simulation Round: Participants used simulation software to analyze and optimize circuit performance under different conditions. This round tested participants' ability to interpret simulation results and make informed design decisions.

Troubleshooting Round: This round presented participants with malfunctioning circuits, and they were required to identify and rectify faults within a stipulated time frame. Effective problem diagnosis, logical reasoning, and swift decision-making were crucial in this round.

Presentation Round: Finalists presented their circuit designs, analysis techniques, and problem-solving approaches to a panel of judges. Participants were evaluated based on the clarity of their presentation, depth of understanding, and ability to articulate their ideas effectively.

Outcome:

PAPER PRO achieved its objective of providing a platform for participants to showcase their skills and knowledge in electrical engineering. The event not only assessed participants' technical proficiency but also encouraged collaboration, as participants exchanged ideas and strategies throughout the competition.

Moreover, PAPER PRO fostered a spirit of innovation and creativity by challenging participants to devise efficient solutions to real-world problems. Participants gained valuable insights into the practical application of electrical engineering principles and developed critical thinking skills essential for the field.

Conclusion:

PAPER PRO, as part of “**Zrotriya**” played a pivotal role in promoting excellence in electrical engineering by nurturing talent, encouraging skill development, and fostering a culture of innovation. The event provided participants with a platform to demonstrate their abilities, collaborate with peers, and gain invaluable experience in tackling real-world engineering challenges. Moving forward, initiatives like PAPER PRO are essential for nurturing the next generation of electrical engineers and driving technological advancement in the field.

S.NO	NAME	ID NUMBER	BRANCH	
1	D.SANDEEP REDDY	2100040151	ECE	P. Sandeep
2	D UMA NAGA SAI	2100040158	ECE	Chamaraga Sai
3	K.SRI VALLIKA	2100040141	ECE	Vallika
4	G SASI NAGA KIRAN	2100040165	ECE	G. Sasi
5	P SUMANTHI	2100049008	ECE	Sumantha. P
6	K SAI TEJA	2100040137	ECE	K. Saiteja
7	M LAVANYA	2100040180	ECE	M. Lavanya
8	D SRAVANI	2100040227	ECE	Sravani. D
9	S SRI RAM	2100040157	ECE	S. Sri Ram
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14	R KARTHIK	2100040134	ECE	Rim Basheer
15	P AASRITHA	2100040240	ECE	Aashritha
16	B YESHASWINI	2100040347	ECE	Yeshaswini
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37	JYOTHSNA. K	2200040195	ECE	Jyothsna. K
38	VASHMI	2200040157	ECE	Vashmi
39	HARIHARAN	2200040063	ECE	Hariharan
40	R. SIVESH	2200040033	ECE	R. Sivesh
41	T. RAJESH	2200040257	ECE	T. Rajesh
42	NAGA SAI RAM. G	2200040109	ECE	Naga Sai Ram. G
43	HARSHITHA. D	2200040212	ECE	Harshitha. D
44	MANJU VANIK	2200040057	ECE	Manju Vanik
45	SAI LAKSHMI	2200049054	ECE	Sai Lakshmi
46	KAMALESH	2200049078	ECE	Kamalesh
47	T.M. MANOJ	2200040360	ECE	T. Manoj
48	SONY SRI SUBHA	2200040077	ECE	Sony Sri Subha
49	ANKA RAO	2200040096	ECE	Anka Rao
50	HARI PRASAD	2200040028	ECE	Hari Prasad
51	vevak	2200040214	ECE	vevak



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Admin Off: 29-36-38, Museum Road, Governorpet, Vijayawada - 520 002. Ph: +91 - 866 - 3500122, 2576129

Date of the Event : 01-03-2023
Name of the Event : POSTER PRESENTATION
Venue : R104(Lab)
No. of Participants : Students: 51, Staff: 04

A Report on POSTERPRESENTATION

on 01-03-2023

Introduction:

Zrotriya, a prestigious national-level techno fest, organized by Team Zrotriya, hosted an exhilarating event centered around poster designing. The event aimed to provide a platform for participants to showcase their creativity, innovation, and design skills through visually captivating posters. The Poster Presentation event not only celebrated artistic expression but also encouraged participants to communicate complex ideas effectively through graphic design.

Objective:

The primary objective of the Poster Presentation event was to foster creativity and innovation among participants while emphasizing the importance of visual communication in conveying technical concepts. By challenging participants to design posters on diverse themes related to science, technology, engineering, and mathematics (STEM), the event aimed to promote interdisciplinary collaboration and inspire novel approaches to problem-solving.

Description:

KL Deemed to be University (Koneru Lakshmaiah Education Foundation) Department of ECE conducted “POSTER PRESENTATION” program on 01-03-2024 at R&D 104(LAB). We started the event at 9:30AM.

With the intention of educating the students regarding basic electronics, Students are very eagerly learning and were very interested in taking part in their given problem statements.

Under the guidance of B Sai Sandeep,G L P Ashok, this event was very successful. Mr. P. Srikanth Reddy (Program Coordinator, KLEF PULSE) also took part in the event and together made the event a grand success.

The outcomes of “**POSTER PRESENTATION**” encompassed the recognition of winners for their prowess in electrical circuit design, analysis, and troubleshooting, alongside the enhancement of participants' skills and knowledge in the field. Networking opportunities facilitated valuable connections among enthusiasts, students, educators, and professionals, fostering collaboration and knowledge exchange. Feedback collected from stakeholders aimed at refining future editions, ensuring continual improvement. Moreover, the event served as a source of inspiration and motivation, encouraging participants to pursue further exploration and opportunities within the realm of electrical engineering.

Event Photos:



Here we can observe by these two picture that our Honorable faculty members were involved in this event and they came front to interview and conducted a HR round for the students.



In this picture we made the students as a groups as their wish and give a troubleshoot to solve it, meanwhile faculty came to take part in it.

Moment was captured in a single frame, encapsulating the essence of the POSTER PRESENTATION event. In the photograph, a group of enthusiastic participants is seen engaged in an animated discussion, surrounded by stacks of papers and research materials. The intense expressions on their faces reflect the intellectual fervor and passion for knowledge exchange that permeated the event. With hands gesturing emphatically and minds abuzz with ideas, the participants epitomize the spirit of academic inquiry and collaboration fostered by POSTER PRESENTATION. This snapshot serves as a poignant reminder of the profound impact of scholarly endeavors and the transformative power of sharing knowledge within the dynamic milieu of Zrotriya.

JUDGES :

- 1)Dr K. Srinivasa Rao
- 2) Dr.Phani Kishore
- 3)Dr.Rehman
- 4)Dr.K.S.Ramesh

Event Structure:

POSTER PRESENTATION comprised several competitive rounds designed to test participants' capabilities across different areas of electrical engineering. The event featured challenges ranging from basic circuit design to complex troubleshooting scenarios, ensuring a comprehensive assessment of participants' skills.

Preliminary Round: Participants were required to solve theoretical problems related to electrical circuits, demonstrating their understanding of fundamental concepts.

Design Challenge: In this round, participants were tasked with designing circuits to meet specific requirements, such as voltage regulation, signal amplification, or frequency filtering. Creativity, efficiency, and adherence to design constraints were key evaluation criteria.

Simulation Round: Participants used simulation software to analyze and optimize circuit performance under different conditions. This round tested participants' ability to interpret simulation results and make informed design decisions.

Troubleshooting Round: This round presented participants with malfunctioning circuits, and they were required to identify and rectify faults within a stipulated time frame. Effective problem diagnosis, logical reasoning, and swift decision-making were crucial in this round.

Presentation Round: Finalists presented their circuit designs, analysis techniques, and problem-solving approaches to a panel of judges. Participants were evaluated based on the clarity of their presentation, depth of understanding, and ability to articulate their ideas effectively.

Outcome:

POSTER PRESENTATION achieved its objective of providing a platform for participants to showcase their skills and knowledge in electrical engineering. The event not only assessed participants' technical proficiency but also encouraged collaboration, as participants exchanged ideas and strategies throughout the competition.

Moreover, POSTER PRESENTATION fostered a spirit of innovation and creativity by challenging participants to devise efficient solutions to real-world problems. Participants gained valuable insights into the practical application of electrical engineering principles and developed critical thinking skills essential for the field.

Conclusion:

POSTER PRESENTATION, as part of “**Zrotriya**” played a pivotal role in promoting excellence in electrical engineering by nurturing talent, encouraging skill development, and fostering a culture of innovation. The event provided participants with a platform to demonstrate their abilities, collaborate with peers, and gain invaluable experience in tackling real-world engineering challenges.



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Admin Off: 29-36-38, Museum Road, Governorpet, Vijayawada - 520 002. Ph: +91 - 866 - 3500122, 2576129

Date of the Event : 28-02-2023
Name of the Event : PROJECT EXPO
Venue : R104(Lab)
No. of Participants : ALL Final Year students

A Report on PROJECT EXPO

on 28-02-2023

Introduction:

Zrotriya, a prestigious national-level techno fest, organized by Team Zrotriya, hosted an exhilarating event centered around poster designing. The event aimed to provide a platform for participants to showcase their creativity, innovation, and design skills through visually captivating posters. PROJECT EXPO event not only celebrated artistic expression but also encouraged participants to communicate complex ideas effectively through graphic design.

Objective:

The primary objective of PROJECT EXPO was to offer participants an opportunity to present their innovative projects and research endeavors to a wider audience. The event aimed to encourage knowledge sharing, inspire creativity, and recognize outstanding achievements in various fields of technology. Additionally, PROJECT EXPO aimed to promote interdisciplinary collaboration and facilitate networking among participants, industry professionals, and academia.

Description:

KL Deemed to be University (Koneru Lakshmaiah Education Foundation) Department of ECE conducted “PROJECT EXPO ” program on 28-02-2023 at R&D 104(LAB). We started the event at 9:30AM.

With the intention of educating the students regarding basic electronics, Students are very eagerly learning and were very interested in taking part in their given problem statements.

Under the guidance of K.Sriathi Roy this event was very successful. Mr. P. Srikanth Reddy (Program Coordinator, KLEF PULSE) also took part in the event and together made the event a grand success.

The outcomes of “**PROJECT EXPO** ” encompassed the recognition of winners for their prowess in electrical circuit design, analysis, and troubleshooting, alongside the enhancement of participants' skills and knowledge in the field. Networking opportunities facilitated valuable connections among enthusiasts, students, educators, and professionals, fostering collaboration and knowledge exchange. Feedback collected from stakeholders aimed at refining future editions, ensuring continual improvement. Moreover, the event served as a source of inspiration and motivation, encouraging participants to pursue further exploration and opportunities within the realm of electrical engineering.

By challenging participants to design posters on diverse themes related to science, technology, engineering, and mathematics (STEM), the event aimed to promote interdisciplinary collaboration and inspire novel approaches to problem-solving.

Event Photos:



Here we can observe by these two picture that our Honorable faculty members wereinvolved in this event and they came front to interview and conducted a HR round



In this picture we made the students as a groups as their wish and give a troubleshoot to solve it, meanwhile faculty came to take part in it.

Moment was captured in a single frame, encapsulating the essence of PROJECT EXPO event. In the photograph, a group of enthusiastic participants is seen engaged in an animated discussion, surrounded by stacks of papers and research materials.

Event Structure:

Project Expo featured an array of innovative projects spanning multiple disciplines, including but not limited to computer science, engineering, biotechnology, and sustainability. The event comprised the following components:

Project Exhibits:

Participants set up booths to showcase their projects, providing detailed demonstrations and explanations to visitors. The exhibits offered a hands-on experience, allowing attendees to interact with the projects and understand their functionalities.

PROJECT EXPO :

In addition to physical exhibits, participants prepared posters summarizing their projects' objectives, methodologies, and outcomes. PROJECT EXPO s provided a concise overview of the projects and facilitated discussions among participants and visitors. participants were tasked with designing circuits to meet specific requirements, such as voltage regulation, signal amplification, or frequency filtering. Creativity, efficiency, and adherence to design constraints were key evaluation criteria.

Technical Talks and Workshops:

Project Expo featured technical talks and workshops conducted by industry experts and academic scholars. Topics covered a wide range of subjects, including emerging technologies, research methodologies, and career opportunities in various fields.

Judging and Awards Ceremony:

A panel of esteemed judges evaluated the projects based on criteria such as innovation, technical merit, practical applicability, and presentation quality. Winners were recognized and awarded prizes during the closing ceremony, acknowledging their contributions to the technological landscape.

Outcomes:

Project Expo at Zrotriya facilitated knowledge exchange, collaboration, and inspiration among participants and attendees. The event provided a platform for emerging talents to gain visibility, receive feedback, and forge valuable connections within the tech community. Moreover, Project Expo promoted innovation and encouraged participants to explore new frontiers in technology, addressing real-world challenges and driving positive change.

By showcasing a diverse range of projects, Project Expo highlighted the significance of interdisciplinary collaboration and the transformative power of technology in addressing global issues. Participants gained invaluable experience in project management, communication, and problem-solving, fostering their personal and professional development. The intense expressions on their faces reflect the intellectual fervor and passion for knowledge exchange that permeated the event. With hands gesturing emphatically and minds abuzz with ideas, the participants epitomize

Project Expo emerged as a cornerstone event within Zrotriya, encapsulating the essence of innovation, collaboration, and excellence in technology. The event not only celebrated the achievements of participants but also inspired future generations to pursue their passion for technology and make meaningful contributions to society. Moving forward, initiatives like Project Expo are instrumental in nurturing talent, fostering innovation, and shaping the future of technology on a global scale.

Moreover, PROJECT EXPO fostered a spirit of innovation and creativity by challenging participants to devise efficient solutions to real-world problems. Participants gained valuable insights into the practical application of electrical engineering principles and developed critical thinking skills essential for the field.

Zrotriya's PROJECT EXPO Event: A dynamic showcase of creativity and innovation, where participants craft visually stunning posters to communicate complex STEM concepts effectively. Empowering participants to unleash their design talents and express their ideas visually through PROJECT EXPO event at the national-level techno fest, Zrotriya



Koneru Lakshmaiah Education Foundation

(Category -1, Deemed to be University estd. u/s. 3 of the UGC Act, 1956)

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Date of the Event : 28-02-2023
Name of the Event : TI WorkShop
Venue : R104(Lab)
No. of Participants : Students: 51, Staff: 04

A Report on TI WORKSHOP

on 28-02-2023

Objective:

The objective of TI WORKSHOP was to provide participants with a platform to showcase their proficiency in electrical circuit design, analysis, and troubleshooting, fostering learning, skill development, collaboration, and inspiration within the field of electrical engineering.

Description: KL Deemed to be University (Koneru Lakshmaiah Education Foundation) Department of ECE conducted “TI WORKSHOP” program on 28-02-2023 at R&D 104(LAB). We started the event at 9:30AM.

With the intention of educating the students regarding basic electronics, Students are very eagerly learning and were very interested in taking part in their given problem statements.

Under the guidance of N.Durga Indira, Y.Usha Devi, this event was very successful. Mr. P. Srikanth Reddy (Program Coordinator, KLEF PULSE) also took part in the event and together made the event a grand success.

The outcomes of TI WORKSHOP encompassed the recognition of winners for their prowess in electrical circuit design, analysis, and troubleshooting, alongside the enhancement of participants' skills and knowledge in the field. Networking opportunities facilitated valuable connections among enthusiasts, students, educators, and professionals, fostering collaboration and knowledge exchange. Feedback collected from stakeholders aimed at refining future editions, ensuring continual improvement. Moreover, the event served as a source of inspiration and motivation, encouraging participants to pursue further exploration and opportunities within the realm of electrical engineering.

Event Photos:



Here we can observe by these two picture that our Honorable faculty members were involved in this event and they came front to interview and conducted a HR round for the students.



In this picture we made the students as a groups as their wish and give a troubleshoot to solve it, meanwhile faculty came to take part in it.

Event Structure:

TI WORKSHOP comprised several competitive rounds designed to test participants' capabilities across different areas of electrical engineering. The event featured challenges ranging from basic circuit design to complex troubleshooting scenarios, ensuring a comprehensive assessment of participants' skills.

Preliminary Round: Participants were required to solve theoretical problems related to electrical circuits, demonstrating their understanding of fundamental concepts.

Design Challenge: In this round, participants were tasked with designing circuits to meet specific requirements, such as voltage regulation, signal amplification, or frequency filtering. Creativity, efficiency, and adherence to design constraints were key evaluation criteria.

Simulation Round: Participants used simulation software to analyze and optimize circuit performance under different conditions. This round tested participants' ability to interpret simulation results and make informed design decisions.

Troubleshooting Round: This round presented participants with malfunctioning circuits, and they were required to identify and rectify faults within a stipulated time frame. Effective problem diagnosis, logical reasoning, and swift decision-making were crucial in this round.

Presentation Round: Finalists presented their circuit designs, analysis techniques, and problem-solving approaches to a panel of judges. Participants were evaluated based on the clarity of their presentation, depth of understanding, and ability to articulate their ideas effectively.

Outcome:

TI WORKSHOP achieved its objective of providing a platform for participants to showcase their skills and knowledge in electrical engineering. The event not only assessed participants' technical proficiency but also encouraged collaboration, as participants exchanged ideas and strategies throughout the competition.

Moreover, TI WORKSHOP fostered a spirit of innovation and creativity by challenging participants to devise efficient solutions to real-world problems. Participants gained valuable insights into the practical application of electrical engineering principles and developed critical thinking skills essential for the field.

Conclusion:

TI WORKSHOP, as part of “**Zrotriya**” played a pivotal role in promoting excellence in electrical engineering by nurturing talent, encouraging skill development, and fostering a culture of innovation. The event provided participants with a platform to demonstrate their abilities, collaborate with peers, and gain invaluable experience in tackling real-world engineering challenges. Moving forward, initiatives like TI WORKSHOP are essential for nurturing the next generation of electrical engineers and driving technological advancement in the field.

Culturals :





P. J. Sreenivasulu Reddy
Convenor

M. Suresh
Head of the Department